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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/035,747	12/28/2001	Guy L. Steele JR.	06502.0364	3637

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FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER
LLP
901 NEW YORK AVENUE, NW
WASHINGTON, DC 20001-4413

EXAMINER

MAI, TAN V

ART UNIT	PAPER NUMBER
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2193

DATE MAILED: 06/06/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/035,747

Applicant(s)

STEELE, GUY L.

Examiner

Tan V. Mai

Art Unit

2193

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 15 February 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-54 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-54 is/are rejected.
- 7) ☐ Claim(s) 6 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>11/411/23&12/23/05</u> . | 6) <input type="checkbox"/> Other: _____ |

1. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

2. Claims 1-5, 21-31 and 33-54 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Huang et al (Applicant's admission Prior Art).

Rejection grounds continue to be those set forth in the previous office action (Paper No. dated 10/15/04, paragraph 4).

3. Claims 1-5, 21-31 and 33-54 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Lynch et al (Applicant's admission Prior Art).

Rejection grounds continue to be those set forth in the previous office action (Paper No. dated 10/15/04, paragraph 5).

4. Claims 6-20 and 32 are rejected under 35 U.S.C. 103(a) as being unpatentable over Huang et al (Applicant's admission Prior Art).

Rejection grounds continue to be those set forth in the previous office action (Paper No. dated 10/15/04, paragraph 7).

5. Claims 6-20 and 32 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lynch et al (Applicant's admission Prior Art).

Rejection grounds continue to be those set forth in the previous office action (Paper No. dated 10/15/04, paragraph 8).

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6. Applicant's arguments filed on 2/15/05 have been fully considered but they are not persuasive.

Applicant, in his remarks, argues that:

(1). "[i]n order to anticipate the invention as claimed, Huang must recite each and every element of Claim 1. Applicant respectfully submits Huang does not recite each and every element of Claim 1. For example, Huang at least does not disclose embedded status information. Furthermore, Applicant respectfully submits that the Examiner does state that Huang recites each and every element of Claim 1. (See Office Action, page 2, lines 15-19.) For example, the Examiner does state that Huang discloses embedded status information";

(2). "[i]n order to anticipate the invention as claimed, Lynch must recite each and every element of Claim 1. Applicant respectfully submits Lynch does not recite each and every element of Claim 1. For example, Lynch at least does not disclose embedded status information. Furthermore, Applicant respectfully submits that the Examiner does state that Lynch recites each and every element of Claim 1. (See Office Action, page 3, lines 5-8.) For example, the Examiner does state that Lynch discloses embedded status information";

(3). "[r]egarding Claim 16, the Examiner stated that Huang does not specifically disclose the claimed "floating point operand data" having "sign," "exponent," and "fraction" information. (See Office Action, page 4, lines 3-5.) In addition, the Examiner merely states that Huang's device is a floating point device. (See Office Action, page 4, line 5.) As a result, Applicant respectfully suggests that the Examiner has failed to make a prima facie case of obviousness. In order to make a prima facie case of obviousness, the Examiner must set forth prior art which teach or suggest every claim limitation"; and

(4). "[r]egarding Claim 16, the Examiner stated that Lynch does not specifically disclose the claimed "floating point operand data" having "sign," "exponent," and "fraction" information. (See Office Action, page 4, lines 14-16.) In addition, the Examiner merely states that Lynch's device is a floating point device. (See Office Action, page 4, line 16.) As a result, Applicant respectfully suggests that the Examiner has failed to make a prima facie case of obviousness. In order to make a prima facie case of obviousness, the Examiner must set forth prior art which teach or suggest every claim limitation" (emphasis added).

With respect to the arguments, the examiner carefully reviews all the applied references and the claimed invention.

First, in the previous office action (Paper No. dated 10/15/04, paragraph 4), it is stated "arithmetic calculation circuit (100), including: X and Y operand registers 116 &

118; arithmetic section 114; memory (register file) 112; and special operand generator 122. Each operand register (X or Y) has a first portion (116-1, 118-1) and second portion (116-2, 118-2)" (page 2, lines 16-19). The examiner believes that each "operand register" should have the claimed "first portion" and "second portion ... having embedded status" because Hwang et al teach a "floating point device". Therefore, the operand register should stored "exponent portion", "fraction portion" and embedded status information "tag value storage portion". Therefore, the rejection is still proper.

Second, in the previous office action (Paper No. dated 10/15/04, paragraph 5), it is stated "**floating point unit (36)**, including: **Register Stack (84) and FPU Core (94)**, **FPU Core (94)**, **FPU control (92)**, **Assembly Queue (80)**. The Register Stack (84) has a first portion (87) and second portion (89)" (page 3, lines 6-8). The examiner believes that "Register stack" should have the claimed "first portion" and "second portion ... having embedded status" because Lynch et al teach a "floating point device". Therefore, the "Register stack" should stored "exponent portion", "fraction portion" and embedded status information "tag value(s)". Therefore, the rejection is still proper.

Third, the examiner believes that a "floating point data" should have "sign", "exponent" and "fraction" information". Therefore, the rejection is still proper.

Fourth, the examiner believes that a "floating point data" should have "sign", "exponent" and "fraction" information". Therefore, the rejection is still proper.

7. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tan V. Mai whose telephone number is (571) 272-3726. The examiner can normally be reached on Mon-Wed and Fri. from 9:30am to 2:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kakali Chaki, can be reached on (571) 272-3719. The fax phone number for the organization where this application or proceeding is assigned is:

Official (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (571) 272-2100.



Tan V. Mai
Primary Examiner